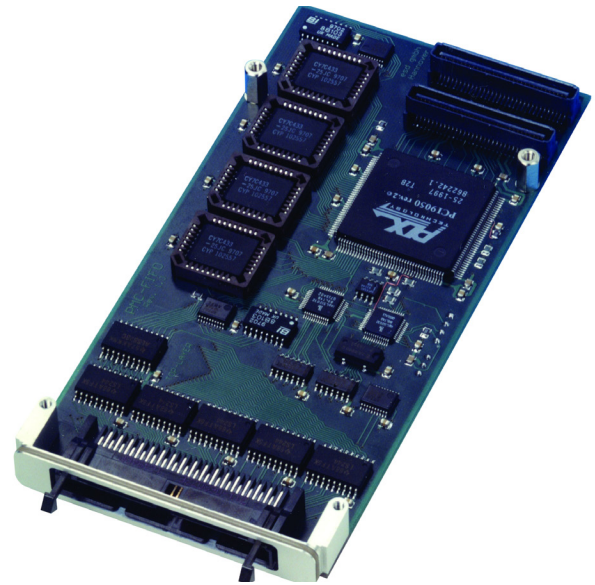




PMC-FIFO

2x 16 Bits FIFO Inputs, 16 TTL Inputs

- 2 FIFO-Ports 16 Bits Wide Each
- Digital Inputs and Outputs



PMC-FIFO

2 FIFO-Ports 16 Bits Wide Each

2x 16 FIFO inputs for TTL level signals support the module PMC-FIFO. The FIFOs can be equipped as 4k-size, 16k-size or compatible circuits. Two externally available write inputs read-in the FIFO inputs, and two RESET inputs can delete the FIFOs externally. For a flexible read-out, the FIFOs can generate interrupts at 'Empty', 'Half-Full', and 'Full'.

Digital Inputs and Outputs

The module has another 16 inputs and 8 outputs that TTL-registers or TTL-drivers read or set; 8 of the inputs can trigger an interrupt on the module.

Defined Power-On Performance

A power-on or receipt of the RESET signal deletes all FIFOs and interrupts and the 8 digital outputs are set to 'high'.

Wiring

A 68-pole socket in the front panel controls the TTL-inputs and outputs.

Software Support

C-drivers in Source-Code are available for the operating systems OS9, VxWorks and RTOS-UH. The drivers offer services for the configuration of the FIFOs, for interrupt control and for reading and setting the inputs and outputs.

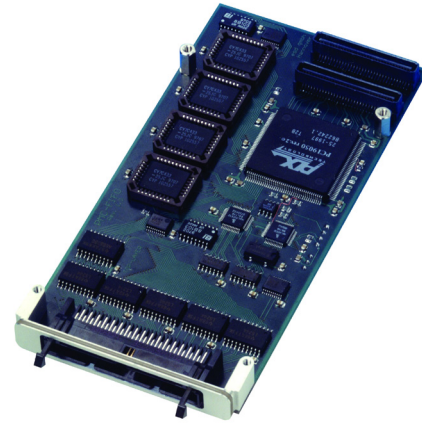
For further information please visit
<http://www.esd-electronics-usa.com/PMC.html>

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PMC-FIFO

2x 16 Bits FIFO Inputs, 16 TTL Inputs

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PMC-FIFO

Technical Specifications

FIFOs	
Number of FIFO inputs	2x 16 (each either 4 K or 16 K depth, receiver 74LS244)
	2x FIFO-write, 2x FIFO-RESET, receiver 74LS14)
Number of FIFO outputs at P1	2x FIFO-Full-Flag
Interrupts	Empty, Half-Full, Full
Maximum transmission rates	Write FIFO via P1: 10 MHz. Read FIFO via PCI: 4 Mbyte/s
Digital I/Os without FIFO	
Interrupt inputs	8 inputs, receiver 74LS14
Static inputs	8 inputs, receiver 74LS244
Outputs	8 outputs, transmitter 74ALS537
General	
Ambient temperature	0...70° C
Connectors	PMC: 2x 64-pole interface I/Os - 68-pole pin strips
Voltage supply	Via PMC, 5 V / ca. 500 mA

For further information please visit
<http://www.esd-electronics-usa.com/PMC.html>

Order Information

Designation		Order No.
PMC-FIFO	2x 16 bits FIFO, each 4 k depth 8 bits input (with IRQ), 8 bits input (without IRQ) 8 bits output	V.2010.01
PMC-FIFO-16K	16 K FIFO instead of 4 k(2x usable)	V.2010.10
Drivers		
PMC-FIFO-OS9	Driver as C-Source for OS9	V.2010.50
PMC-FIFO-VxWorks	Driver as C-Source for VxWorks	V.2010.56
PMC-FIFO-RTOS-UH	Driver for RTOS-UH	V.2010.54
Manual		
PMC-FIFO-ME	English user's manual	V.2010.21

